HERCHANT & BOULD

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S/N 10/050621

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

CHENG et al.

Examiner:

Minh N. Trinh

Serial No.:

10/050621

Group Art Unit:

3729

Piled:

January 15, 2002

Docket No.:

08688.0258US01

Title:

METHOD OF FABRICATING A MULTI-LAYER CIRCUIT BOARD

ASSEMBLY

CERTIFICATE UNDER 37 CFR 14(d):

I hereby certify that this paper is being manumitted by facsimile to the U.S. Patent and Trademark Office on November 12

2004.

By: Name: Shawain Risamaun

PETITION FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. §1.136(a), it is respectfully requested that a 1-month extension of time be granted in which to respond to the outstanding Office Action mailed July 16, 2004, said period of response being extended from October 16, 2004 to September 16, 2004.

Please charge the amount of \$110.00 to Deposit Account No. 13-2725 to cover the required extension fee for a large entity.

Respectfully submitted,

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Dated: November 12, 2004

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